



## GOVERNMENT POLYTECHNIC, PURI DEPARTMENT OF ELECTRICAL ENGINEERING

Discipline: <b>ELECTRICAL ENGINEERING</b>	Semester: <b>5<sup>TH</sup></b>	Name of the Teaching Faculty: <b>MRS .LAXMIPRIYA KHUNTIA SR .LECT. IN ELECTRICAL ENGINEERING</b>	
Subject: <b>DIGITAL ELECTRONICS &amp;MICROPROCE SSOR</b>	No. of classes allotted per week: <b>04</b>	Semester From date: <b>01.08.2023</b> To Date: <b>30.11.2023</b>  No. of Weeks: <b>15</b>	
<b>PRE- REQUISITE</b>	Basic knowledge about digital number system and microprocessor with programming		
<b>COURSE OUTCOMES</b>	CO1: Understand the number system and their application CO2: Understand the concept of combinational circuit and its application CO3: Understand the concept of sequential circuit and its application CO4: Introduction to microprocessor and programming using 8085 microprocessor CO5: Basic interfacing concept and details of 8255 PPI		
<b>Week</b>	<b>Class Day</b>	<b>Theory/Practical Topics</b>	<b>DELIVERY METHOD</b>
1 <sup>ST</sup>	1 <sup>ST</sup>	Different number system	Whiteboard
	2 <sup>ND</sup>	Binary addition, subtraction multiplication & division	Whiteboard
	3 <sup>RD</sup>	1's & 2's complement and subtraction using 2's complement	Whiteboard
	4 <sup>TH</sup>	Explanation of importance and application of Binary codes	Whiteboard
2 <sup>ND</sup>	1 <sup>ST</sup>	Boolean algebra and different logic gates	Whiteboard
	2 <sup>ND</sup>	SOP and POS expression and K-map contnd.	Whiteboard
	3 <sup>RD</sup>	SOP and POS expression solving using K-map	Whiteboard
	4 <sup>TH</sup>	<b>QUIZ&amp;ASSIGNMENT-I</b>	Lecture notes
3 <sup>RD</sup>	1 <sup>ST</sup>	Concept of combinational circuit	Whiteboard
	2 <sup>ND</sup>	Half adder circuit and its truth table varification	Whiteboard
	3 <sup>RD</sup>	Half adder implementation using NAND gates only and NOR gates only	Whiteboard
	4 <sup>TH</sup>	Full adder circuit and its application truth table varification	Whiteboard
4 <sup>TH</sup>	1 <sup>ST</sup>	Realize Full adder using Half adder and OR gates	Whiteboard
	2 <sup>ND</sup>	Full Subtractor circuit and its truth table verification	Whiteboard
	3 <sup>RD</sup>	4:1 MUX and 1:4 DMUX	Whiteboard
	4 <sup>TH</sup>	4:1 MUX and 1:4 DMUX problem solving	Whiteboard
5 <sup>TH</sup>	1 <sup>ST</sup>	Binary decimal Encoder and Decoder problem solving	Whiteboard
	2 <sup>ND</sup>	Adder problem solving	Whiteboard
	3 <sup>RD</sup>	Subtractor problem solving contnd	Whiteboard
	4 <sup>TH</sup>	Subtractor problem solving	Whiteboard
6 <sup>TH</sup>	1 <sup>ST</sup>	Two bit Magnitude comparator	Whiteboard
	2 <sup>ND</sup>	Two bit Magnitude comparator Problem solving	Whiteboard
	3 <sup>RD</sup>	<b>QUIZ&amp;ASSIGNMENT-II</b>	Lecture notes
	4 <sup>TH</sup>	Concept of Sequential circuit	Whiteboard

7 <sup>TH</sup>	1 <sup>ST</sup>	Understanding the necessity of clock and its type o triggering	Whiteboard
	2 <sup>ND</sup>	Clocked S R Flip Flop.	Whiteboard
	3 <sup>RD</sup>	Concept of race around condition and study of Master Slave	Whiteboard
	4 <sup>TH</sup>	JK Flip flop	Whiteboard
8 <sup>TH</sup>	1 <sup>ST</sup>	Truth table of D flip flop and T Flip Flop	Lecture notes
	2 <sup>ND</sup>	Modulus counter	Whiteboard
	3 <sup>RD</sup>	4 bit asynchronous counter and its timing diagram	Whiteboard
	4 <sup>TH</sup>	Asynchronous decade counter	Whiteboard
9 <sup>TH</sup>	1 <sup>ST</sup>	4 bit synchronous counter Registers and its types	Whiteboard
	2 <sup>ND</sup>	Working of SISO,SIPO,PISO,PIPO registers and their truth table	Whiteboard
	3 <sup>RD</sup>	<b>QUIZ&amp;ASSIGNMENT-III</b>	Lecture notes
	4 <sup>TH</sup>	Introduction to microprocessor, Microcomputer	Whiteboard
10 <sup>TH</sup>	1 <sup>ST</sup>	Arichitecture,Pin diagram of 8085microprocessor	Whiteboard
	2 <sup>ND</sup>	Stack pointer Interrupt	Whiteboard
	3 <sup>RD</sup>	Instruction of 8085 microprocessor	Whiteboard
	4 <sup>TH</sup>	Counter and time delay	Whiteboard
11 <sup>TH</sup>	1 <sup>ST</sup>	Assembly language programming of 8085 microprocessor	Whiteboard
	2 <sup>ND</sup>	<b>QUIZ&amp;ASSIGNMENT-IV</b>	Lecture notes
	3 <sup>RD</sup>	Basic interfacing concept	Whiteboard
	4 <sup>TH</sup>	Memory mapping	Whiteboard
12 <sup>TH</sup>	1 <sup>ST</sup>	Memory i/o	Whiteboard
	2 <sup>ND</sup>	Functional block diagram and description of 8255 PPI contd	Lecture notes
	3 <sup>RD</sup>	Functional block diagram and description of 8255 PPI	Whiteboard
	4 <sup>TH</sup>	Application of seven segment LED display	Whiteboard
13 <sup>TH</sup>	1 <sup>ST</sup>	Square wave generator	Whiteboard
	2 <sup>ND</sup>	Traffic light controller	Whiteboard
	3 <sup>RD</sup>	<b>QUIZ &amp; ASSIGNMENT-V</b>	Lecture notes
	4 <sup>TH</sup>	PROBLEM SOLVING	Whiteboard
14 <sup>TH</sup>	1 <sup>ST</sup>	PROBLEM SOLVING	
	2 <sup>ND</sup>	PREVIOUS YEAR QUESTIONS DISCUSSION	
	3 <sup>RD</sup>	REVISION	
	4 <sup>TH</sup>	REVISION	
15 <sup>TH</sup>	1 <sup>ST</sup>	PREVIOUS YEAR QUESTIONS DISCUSSION	
	2 <sup>ND</sup>	REVISION	
	3 <sup>RD</sup>	PREVIOUS YEAR QUESTIONS DISCUSSION	
	4 <sup>TH</sup>	REVISION	

**LEARNING RESOURCES:**

01. Fundamental of Digital Electronics by Ananda Kumar, Pill Publication

02. Digital Electronics-Principal & Application by S. K. Mondal, TMH publication 03. Digital Electronics by B. R. Gupta & V. Singhal, S. K. Kateria publication 04 Digital Electronics n by P. Raja, SciTech publication

Sign. of Faculty concerned

  
Principal  
G. PPuri

  
Sign. of HODi/c